

Notice of References Cited	Application/Control No. 10/023,435	Applicant(s)/Patent Under Reexamination WU ET AL.	
	Examiner Vuthe Siek	Art Unit 2825	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wu et al., "On Fixed Edges and Edge-Reconstruction of Series-Parallel Network*," IEEE, Nov. 1998, pp. 707-710.
	V	Chang et al., "Synthesis for Multiple Input Wires Replacement of a Gate for Wiring Consideration," IEEE, Nov. 1999, pp. 115-118.
	W	
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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